

⌘ Favorites

Search	Find	Reverse	Index	Clear
DBs	NIS-PGPUB, USFAT, EPO, JPO			<input checked="" type="checkbox"/> Durate <input checked="" type="checkbox"/> highlight all the terms initially
Default operator:	OR			
<div style="border: 1px solid black; padding: 10px; min-height: 400px;"> <p>"20010019141"</p> </div>				
<div> ▲ RRS form ▲ ISSR form ▼ Insor ▼ Text ▼ HTML </div>				

	U	Document I	Issue Dat	Page	Title	Current O	Current X	Retrieval	Inventor	S	C	P	Z	B	A
1	F	F	US 2001001	2001090	40	Semiconductor device wit	257/295	257/306;	Takahashi, Seiichi	F	F	F	F	F	F

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L11	1	(first adj barrier and second adj barrier and spacer and capacitor and buffer and plug).clm.	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/08/24 17:23